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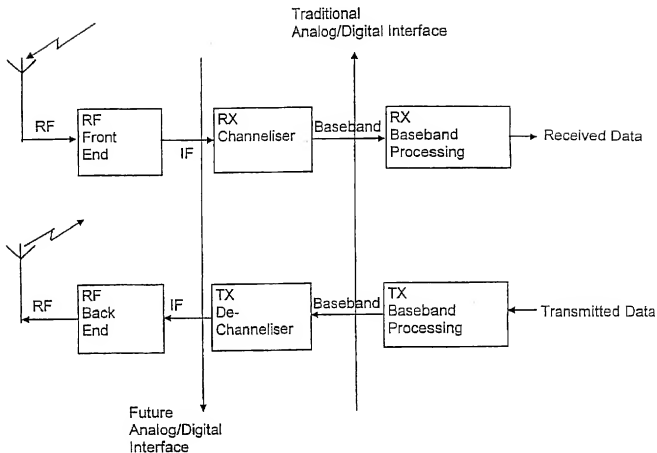


Fig. 1
(Prior Art)

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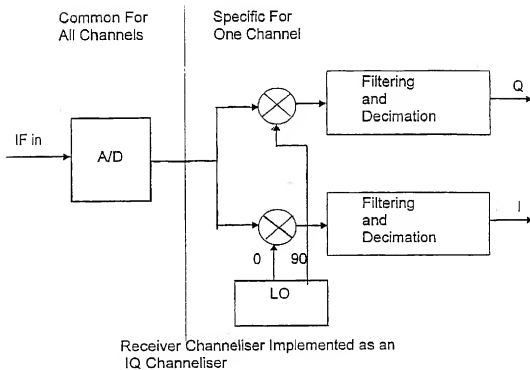
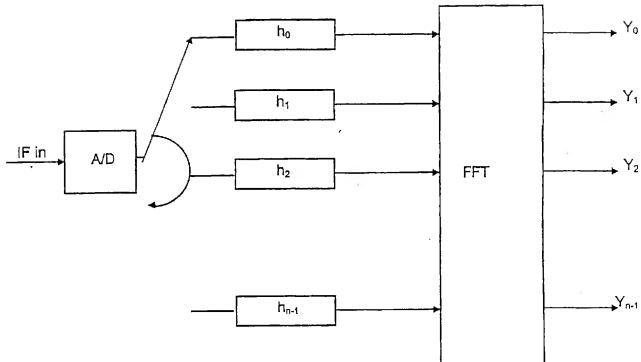


Fig 2
(Prior Art)

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Receiver Channeliser Implemented Via
A Decimated Filter Bank

Fig. 3
(Prior Art)